

## **ABSTRACT OF THE DISCLOSURE**

An embodiment of a graphics device that maintains load balance on a graphics bus when an upgrade graphics device is installed is disclosed. The embodiment includes load balancing buffers for the strobe compliment signals AD\_STB0#, AD\_STB1#, and SB\_STB# on a 2X mode AGP graphics device. The load balancing buffers couple the 2X mode AGP graphics device to the strobe compliment signals AD\_STB0#, AD\_STB1#, and SB\_STB#, but the load balancing buffers are not connected to any internal circuits within the 2X mode AGP graphics device. The load balancing buffers provide equal capacitive loading between the strobe signals AD\_STB0, AD\_STB1, and SB\_STB and their compliment signals AD\_STB0#, AD\_STB1#, and SB\_STB# when an upgrade 4X mode AGP graphics device is installed.